

**Amendments to the Specification:**

*Please replace the paragraph starting at page 12, line 3 with the following amended paragraphs:*

Also, “0” is one input of the MUX 23 connected to the input end of the random ith (herein i is an integer between 2 to n) LSSR, for example, the third LSSR 13 in which i=3, except the first LSSR 11 of 4 LSSRs 11 to 14, and is defined as a value obtained from an OR operation of an output value of the second((i-1)th) shift register 12 with an AND operated value of an output value of the fourth shift register 14 with an ~~output value of the second shift register 12~~ (i-1)th value the second value of the generation polynomial. “1” is another input of the MUX 23 connected to the input end of the third shift register 13 and is defined as a resultant value obtained from an OR operation of first, second and third values. The first value is obtained from an AND operation of an (i-1)th value of the generation polynomial (i.e., the second value of the generation polynomial) and a value from an OR operation of an output signal of the (n-1)th shift register (i.e., the third shift register 13) and a value from an AND operation of an output signal of the nth shift register (i.e., the fourth shift register 14) and the (n-1)th (i.e., the third value) of the generation polynomial, the second value is obtained from an AND operation of an output signal of the nth shift register (i.e., the fourth shift register 14) and the (i-2)th value of the generation polynomial (i.e., the first value of generation polynomial), and the third value

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is an output value of the  $(i-2)$ th shift register (i.e., the first shift register). See also equation 7 for mathematical expressions of the first, second and third values.